

PATENT NUMBER

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SCANNED TR5 O.A. All3	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/810746		710 710	200	2100	Aune Mason

APPLICANTS

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2184

TITLE

Sharing of functions between an embedded controller and a host processor

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ 	_____ (Assistant Examiner)		_____ (Date)	
	NOTICE OF ALLOWANCE MAILED			
	ISSUE FEE			
	_____ (Primary Examiner)		_____ (Date)	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		_____ (Date)	
ISSUE BATCH NUMBER				

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